LISTING OF CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A frequency hopping sequence generator applied to a multi-group frequency hopping frequency division multiple access (FH-FDMA) communication system, comprising:

one binary sequence generator for generating consecutive binary sequences; and

a plurality of non-binary sequence converters for mapping a binary sequence into an independent non-binary sequence for each group of the multiple groups by using a group identifier (ID) assigned to each group, each of the plurality of non-binary sequence converters having a shift register block for receiving the binary sequence and a group ID data storing unit for converting the group ID into binary data.

2. (Currently Amended) The generator of claim 1, wherein the plurality of non-binary sequence converters comprises:

~~a shift register block for receiving the binary sequence;~~

~~a group ID data storing unit for converting the group ID into binary data;~~

a divided band selector for dividing a hopping band as many as groups and allocating the groups to each of the divided bands; and

a detailed hopping binary sequence selector for outputting a value for generation of a detailed hopping sequence in each of the divided bands.

3. (Previously Presented) The generator of claim 2, wherein the divided band selector comprises: an exclusive-OR operator for exclusive-ORing some binary sequences outputted from the shift register block and binary data of the group ID outputted from the group ID data storing unit, and dividing the hopping band.

4. (Previously Presented) The generator of claim 2, wherein the detailed hopping binary sequence selector comprises:

a decoder for decoding the binary data of the group ID; and

a calculating unit for performing a logical operation on an output of the decoder and some of the binary sequences outputted from the shift register block